

PCN / EOL Notification

Product Change Notific	Notification Date: June 29, 2017							
Title: Addition of UMC 8S Wafer Fabrication Facility for All AT25DF321A Products								
Product Identification: All wafer and package options of the Industrial Temperature grade (-40°C to +85°C) AT25DF321A Reason for Change:								
Material / Composition	Design / Firmy	Design / Firmware Manu			nufacturing Location			
wateriar / Composition		Design / Finns	vale	Manuracturii	ig Localio	"		
Processing / Manufacturing		Logistics	Logistics		Quality/Reliability			
Change Description:								
This product is currently manufactured at UMC Fab 8D (Taiwan). Adesto will add UMC Fab 8S (Taiwan) as a wafer fabrication facility for all AT25DF321A family of devices as shown in Table 1. The UMC Fab 8S facility will manufacture the parts to the same datasheet specification as the current UMC Fab 8D. No Datasheet changes are being implemented as a result of this manufacturing location update. UMC Fab 8S parts will be compatible in form, fit, and function to the UMC Fab 8D. Ordering codes will remain unchanged as shown in Table 1.								
<u> </u>	unchange	d as shown in Tab	le 1.					
Table 1 Ordering Code ⁽¹⁾	Package	Lead (Pad) Finish	Operating Voltage	Max. Fre	q. (MHz)	Operation Range		
AT25DF321A-MH-Y AT25DF321A-MH-T	8MA1	NiPdAu		Industrial (-40°C to +85°c				
AT25DF321A-SH-B AT25DF321A-SH-T	882	NIPdAu	2.7V to 3.6V			Industrial (-40°C to +85°C)		
AT25DF321A-CCU-T	9CC1	SnAgCu						
Qualification Data:	Avail	able		Will be available (mm/dd/yr): 09/15/17		Not Applicable		
Samples:	Avail	able		Will be available (mm/dd/yr): 08/15/17		Not Applicable		
Quantifiable Impact on Q None	tuality& Relia	bility:						
Forecasted Availability D Last Time Buy Date: N/ Last Ship Date: N/A		2017						
Adesto Contact: pcn@a	destotech.co	m						

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Comments:	

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